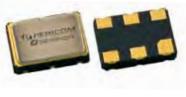


### Ultra Low Jitter PLL Crystal Oscillator 7.0 x 5.0mm

## 2.5V/3.3V CMOS XO

# **JX701**



7.0 x 5.0mm Ceramic SMD

#### Product Features

- Very low phase jitter 0.8ps RMS typical
- Thicker crystal for improved reliability
- Low supply current 45mA max.
- Industrial Temperature Range
- Pb-free & RoHS compliant
- Fast lead time

#### **Product Description**

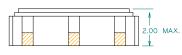
The JX701 XO series is a high performance CMOS crystal oscillator family with very low jitter performance. It supports various options including wider frequency range, 2.5V/3.3V voltage, and various stabilities. It is designed to meet the clock source specifications for communication systems, and other high performance equipment.

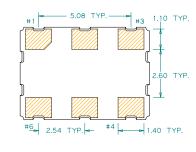
#### **Applications**

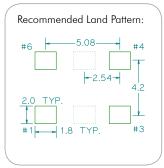
- Networking systems
- Servers and storage systems
- Profession video equipments
- Test and measurement
- FPGA/ASIC clock generation

# 5.00±0.15

Package: (Scale: none, Dimensions are in mm)







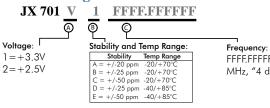
\*Note: pads 2 and 5 are optional (shown as dotted lines). XO's are designed to fit on industry standard, 4 pad layouts.

#### **Pin Functions:**

Pin	Function
1	OE Function
2	N/C
3	Ground
4	Output
5	N/C
6	V <sub>DD</sub>

\*Extended high frequency power decoupling is recommended (see test circuit for minimum recommendation). To ensure optimal performance, do not route RF traces beneath the package.

#### **Part Ordering Information:**



Frequency: FFFF.FFFFFF MHz, "4 digits/decimal/6 digits" format

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